

Outline:

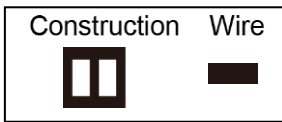
- Low power losses(AC and DC)
- Magnetically shielded
- Low leakage flux noise
- Soft saturation characteristic to withstand high current spikes

Features:

- Core material: Alloy powder
- Environmental:RoHS,Reach compliant ,Halogen free
- Weight:2.06g
- Moisture Sensitivity:Level (MSL) 1
(unlimited floor life at 5~40°C, RH≤70% relative humidity).
- Operating temperature range: -40°C~+125°C
(including coil's self temperature rise)
- Storage temperature range: -40°C~+125°C

Application:

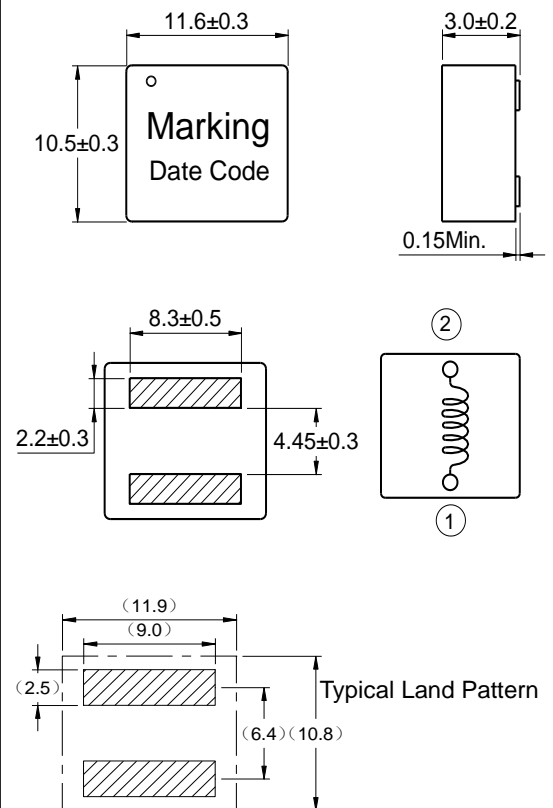
- DC/DC converter for high current power supplies
- DC/DC converter for Field Programmable Gate Array(FPGA)
- Wireless power devices/Filte
- Battery power devices



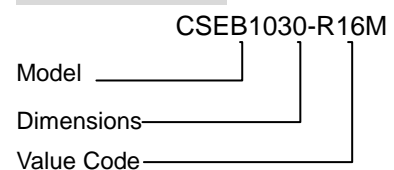
1 Electrical Characteristics

Part No.	Inductance (μ H) $\times 1$ $\pm 20\%$	D.C.R. (m Ω)		Isat (A) $\times 2$ Typical	Irms (A) $\times 3$ Typical
		Typical	Max.		
CSEB1030-R16M	0.16	1.10	1.21	97.0	42.0
CSEB1030-R30M	0.30	1.55	1.70	75.0	35.0
CSEB1030-R56M	0.56	2.50	2.75	48.0	32.0
CSEB1030-1R0M	1.00	4.50	4.95	38.5	23.0
CSEB1030-2R2M	2.20	9.90	13.0	27.5	15.0

2 Product Dimensions (mm)



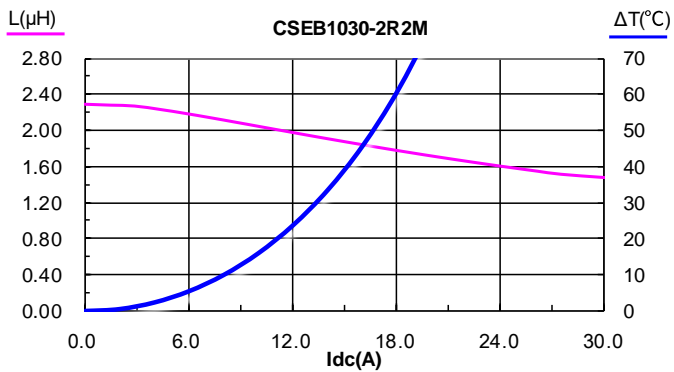
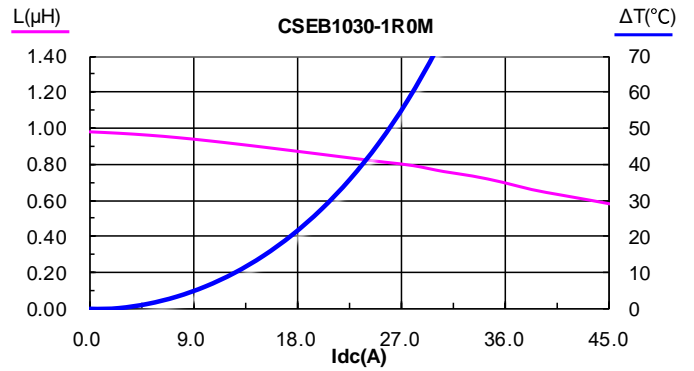
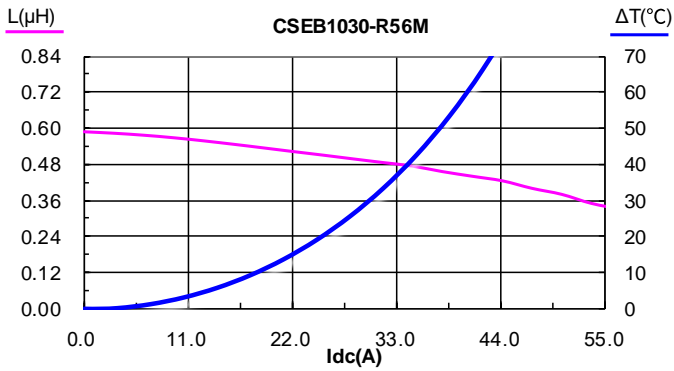
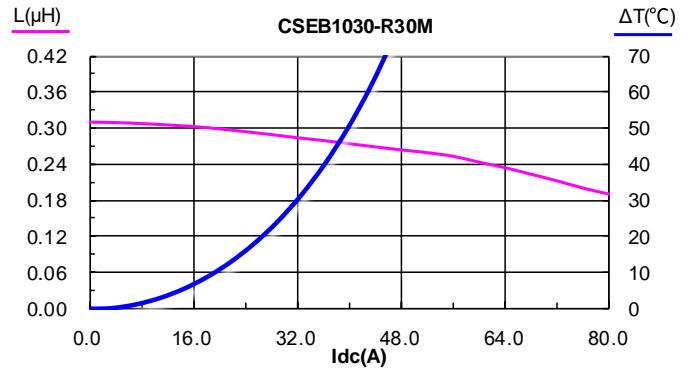
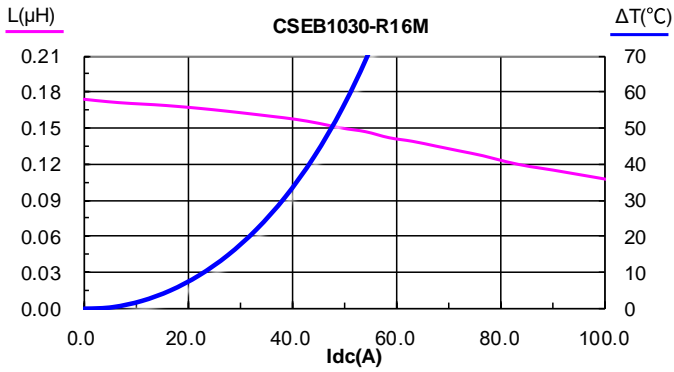
3 How to Order



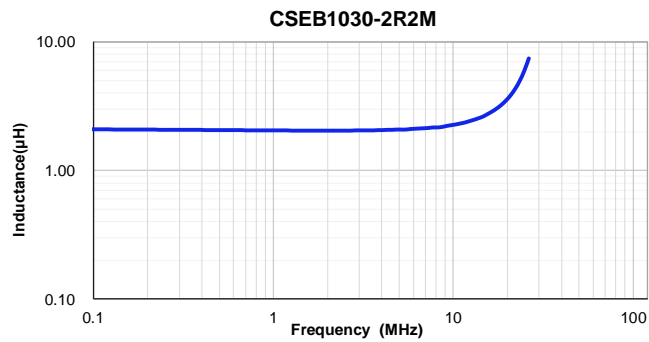
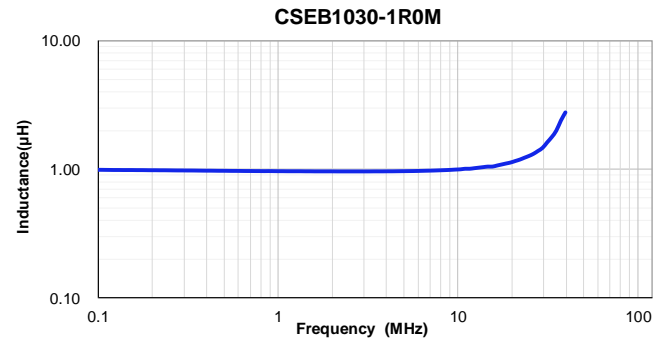
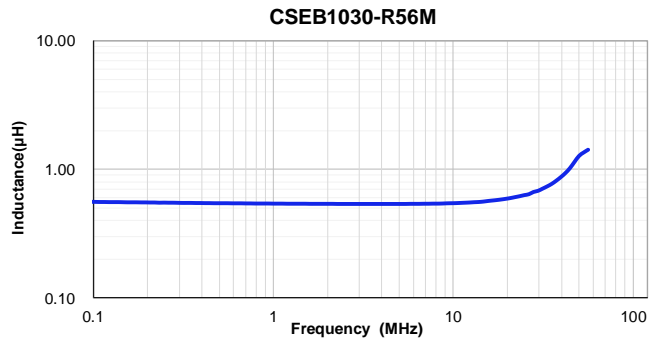
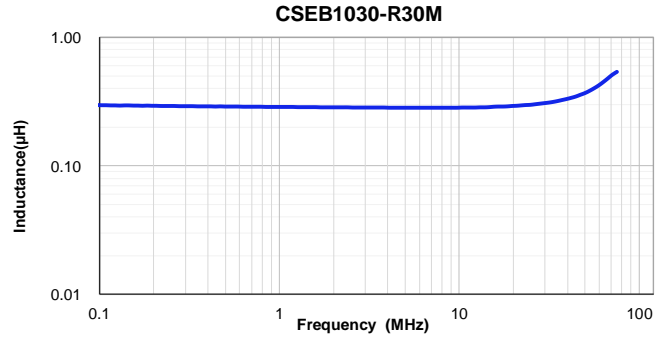
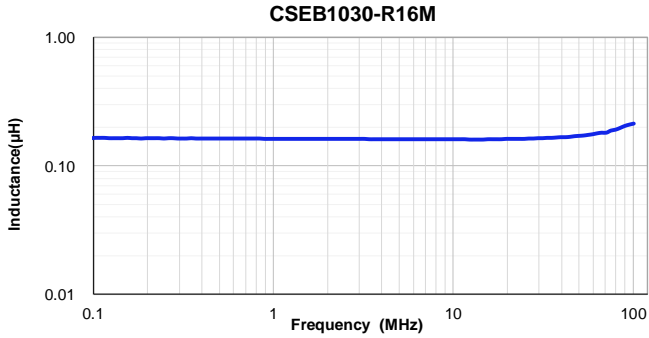
All data is tested on 25°C ambient temperature

1. Inductance measure condition at 100kHz,0.1V
2. Isat: the actual value of DC current when the inductance decrease approximately 30% of its initial value.
3. Irms:the actual value of DC current when the temperature rise is $\Delta T40^{\circ}\text{C}$ ($T_a=25^{\circ}\text{C}$)

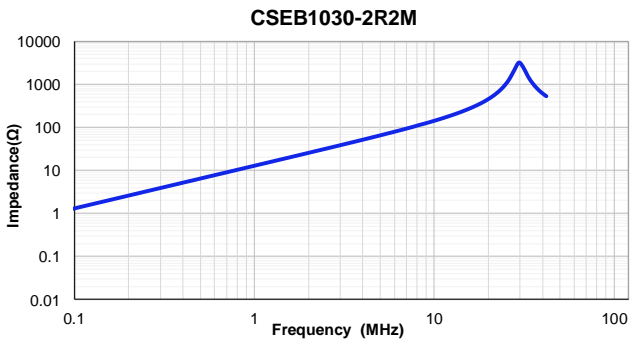
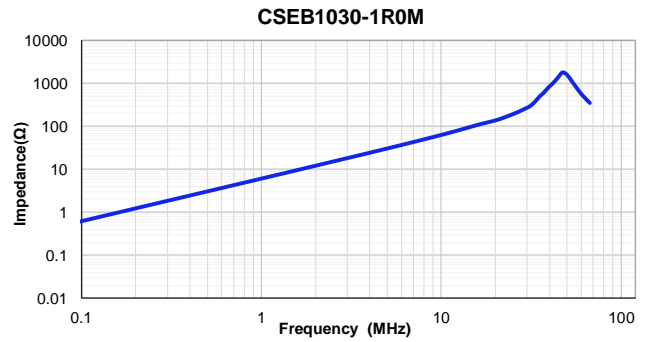
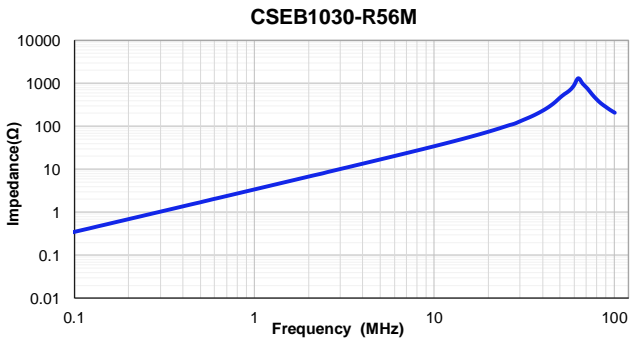
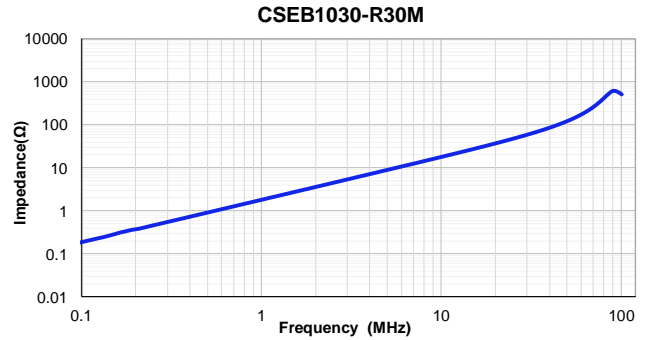
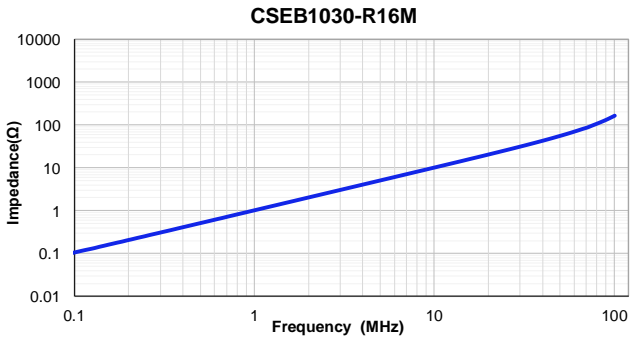
4 Saturation Current vs Temperature Rise Current Curve



5 L vs Frequency

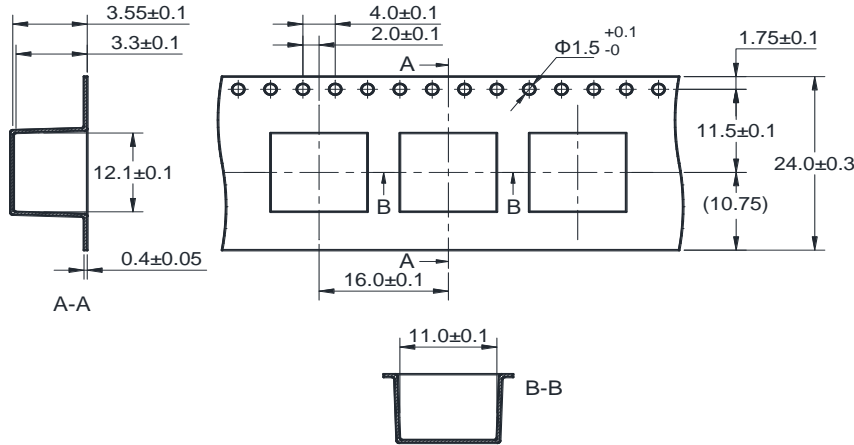


6 Impedance vs Frequency



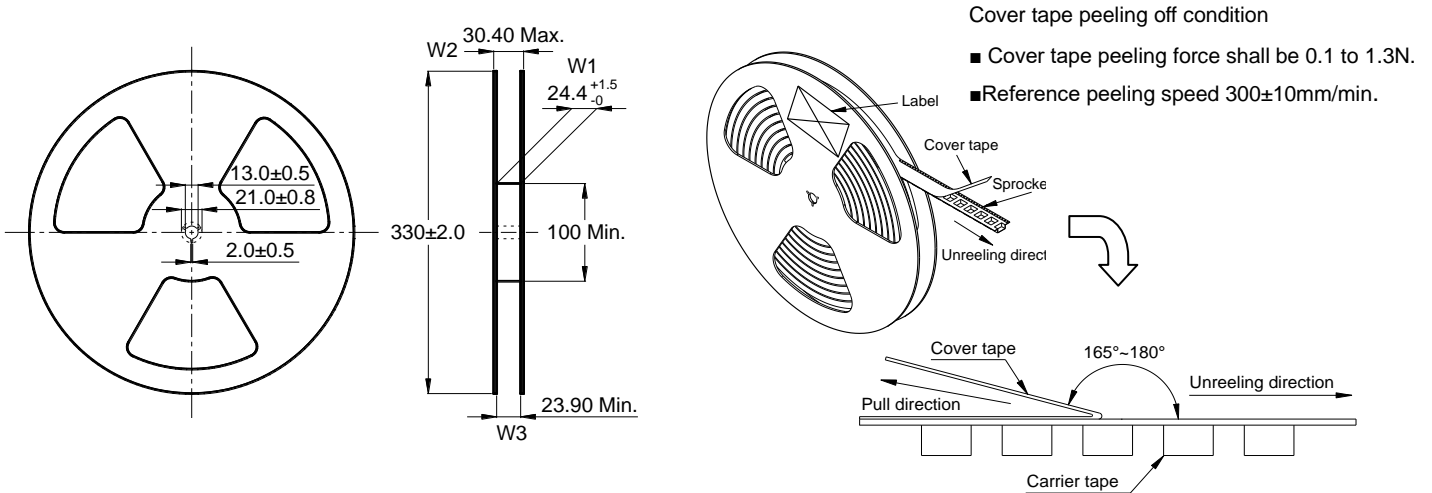
7 Packing Specification

7.1 Carrier Tape Dimensions (mm)



※ Packing is referred to the international standard IEC 60286-3.

7.2 Reel Dimensions (mm)



Cover tape peeling off condition

- Cover tape peeling force shall be 0.1 to 1.3N.
- Reference peeling speed 300±10mm/min.

7.3 Carton Dimensions and Packing Quantity

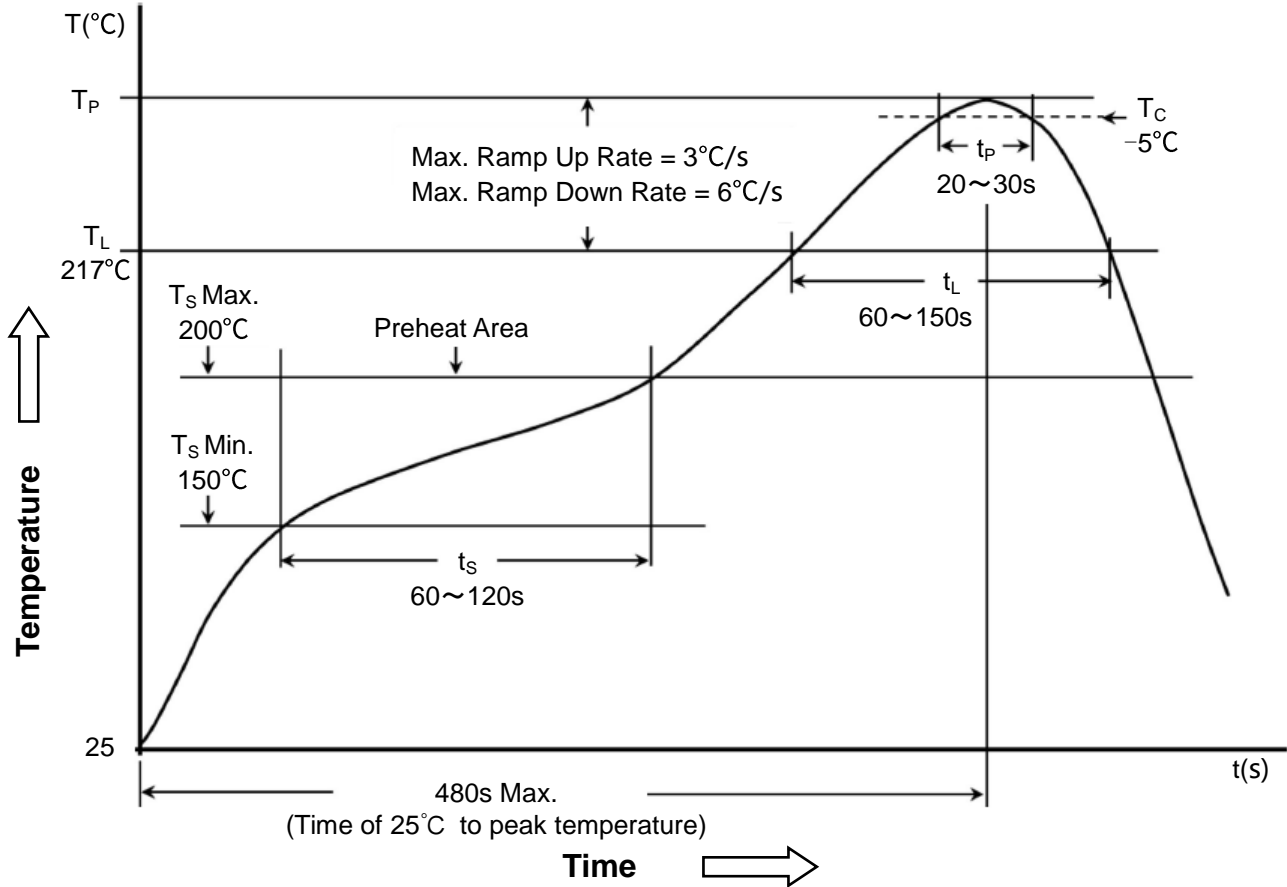
■ Inner Carton: 340×340×95mm

■ Outer Carton : 360×360×370mm

Product Series	Quantity / Reel	Inner Carton Quantity	Outer Carton Quantity
CSEB1030	800pcs	(800×2) = 1600pcs	(1600×3) = 4800pcs

8 Soldering Specification

8.1 Reflow Profile for SMT Components



8.2 Classification of Peak Package Body Temperature (Tp)

	Package Thickness	Package Volume		
		<350 mm ³	350~2000 mm ³	>2000 mm ³
PB-Free Assembly	<1.6mm	260°C	260°C	260°C
	1.6~2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

※ Reflow is referred to standard IPC/JEDEC J-STD-020E.

9 Notice of Use

- 9.1 Special remind:Circuit design, component placement, PCB size and thickness, cooling system and etc. all will affect the product temperature. Please verify the product temperature in the final application.
- 9.2 Product in packing storage condition:temperature 5~40°C, RH≤70%.
If taking out for use, the remaining products should be sealed in plastic bags and preserved in accordance With the above conditions, to avoid oxidation of terminals (electrodes), affecting soldering status.
- 9.3 A storage of Codaca Electronic products for longer than 12 months is not recommended, Within other effects, the terminals may suffer degradation, resulting in bad solderability. Therefore, all products shall be used within the period of 12 months based on the day of shipment.
- 9.4 Do not keep products in unsuitable storage conditions, such as areas susceptible to high temperatures, high humidity, dust or corrosion.
- 9.5 Always handle products with care.
- 9.6 Don't touch electrodes directly with bare hands as oil secretions may inhibit soldering.
Always ensure optimum conditions for soldering.
- 9.7 When this product will be used on a similar or new project to the original one, sometimes it might be unable to satisfy the specifications due to different condition of usage.
- 9.8 This inductor itself does not have any protective function in abnormal condition, such as overload, short-circuit, open-circuit conditions, etc. Therefore, it shall be confirmed that there is no risk of smoke, fire, dielectric withstand voltage, insulation resistance, etc., or use in abnormal conditions protective devices or protection circuit in the end product.
- 9.9 Hi-Pot test with higher voltage than spec value will damage insulating material and shorten its life.
- 9.10 If using in potting compound, the magnet wire coating might be damaged, please consult with us.
- 9.11 Refrain from rinsing product. If necessary, please consult with us.
- *9.12 Codaca Electronic products without "V" prefix are qualified for industrial product requirement , and with "V" prefix are qualified for AEC-Q200, but it doesn't mean that Codaca Electronic products can absolutely meet specific industry norms and quality test standards in automotive electronics or more strict application fields . Codaca Electronic will be exempted from being responsible for the consequences of using Codaca products in automotive electronic or higher application field related to safety when without being aware of it.