

AEC-Q200



Construction Wire



## Outline:

- Flat wire winding, achieve a low DC resistance.
- Magnetic shielded structure: excellent resistance to electro magnetic interference(EMI)
- Soft saturation makes them ideal for VDR/VRM applications

## Features:

- Core material: Composite
- Core and winding loss:  
[www.codaca.com/DesignTool\\_Power-Inductor-Loss-Comparison.html](http://www.codaca.com/DesignTool_Power-Inductor-Loss-Comparison.html)
- Environmental: RoHS, Reach compliant ,Halogen free
- Weight:1.61g
- Moisture Sensitivity:Level (MSL) 1  
(unlimited floor life at <30°C / 85% relative humidity).
- Operating temperature range: -50°C~+150°C  
(including coil's self temperature rise)
- Storage temperature range: -50°C~+150°C

## Application:

- Graphic cards/ industrial computers
- DC /DC converters/Motherboards/Filter
- High current switching regulators/Polyphase-switching regulators

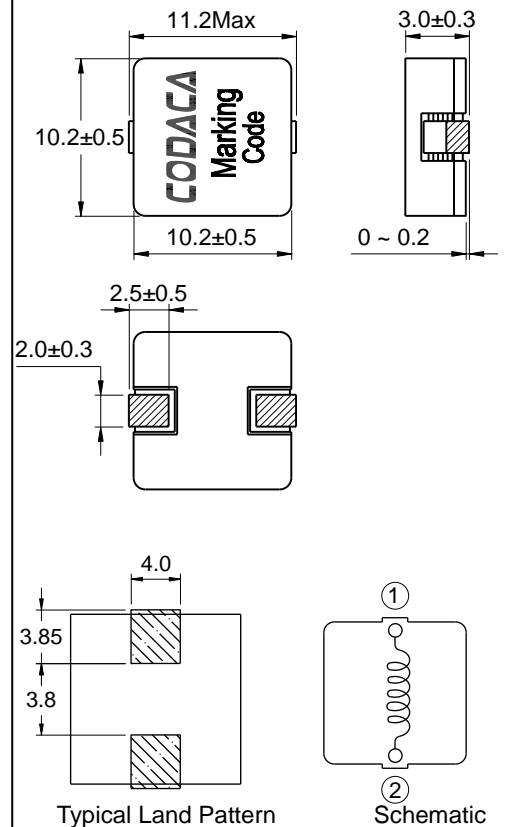
## 1 Electrical Characteristics

Part No.	Inductance ( $\mu$ H)※1 $\pm 20\%$	D.C.R. (m $\Omega$ )		Isat (A)※2 Typical	Irms (A) ※3 Typical
		Typical	Max		
CSBL1030-R20M	0.20	0.82	0.95	50.0	22.0
CSBL1030-R47M	0.47	2.15	2.50	32.0	18.0
CSBL1030-1R0M	1.00	4.08	4.70	21.0	15.0
CSBL1030-1R5M	1.50	6.58	7.60	18.0	12.0
CSBL1030-2R2M	2.20	11.5	13.3	15.0	9.00
CSBL1030-2R5M	2.50	11.5	13.3	12.0	9.00

All data is tested on 25°C ambient temperature

- Inductance measure condition at 100kHz,0.5V
- Isat:the actual value of DC current when the inductance decrease 30% of its initial value
- Irms:the actual value of DC current when the temperature rise is  $\Delta T 50^{\circ}\text{C}$  ( $T_a=25^{\circ}\text{C}$ )

## 2 Product Dimensions (mm)



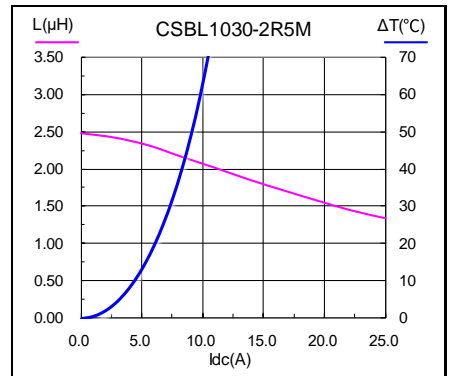
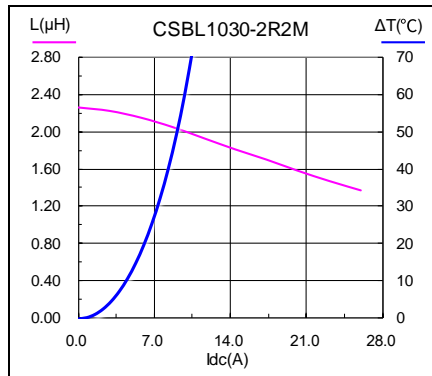
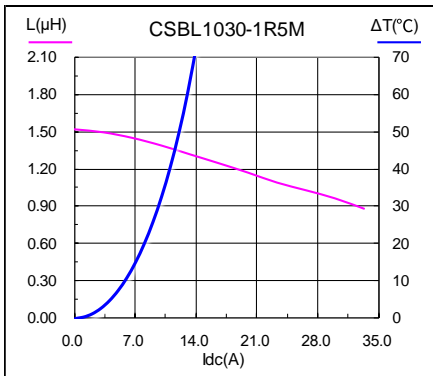
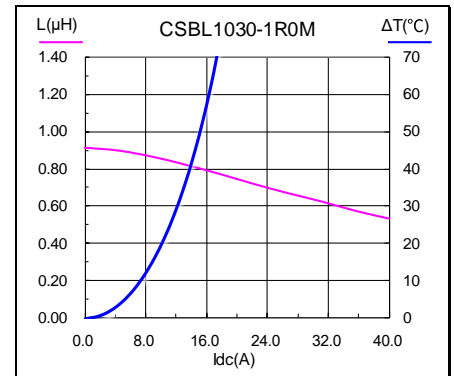
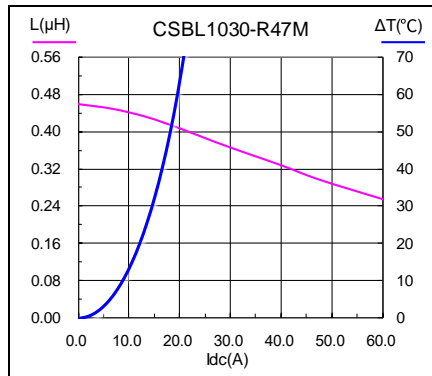
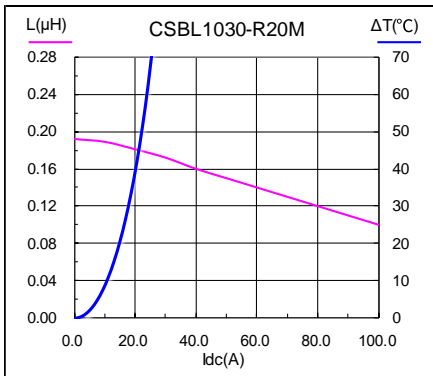
## 3 How to Order

Model \_\_\_\_\_ CSBL1030-R20M

Dimensions \_\_\_\_\_

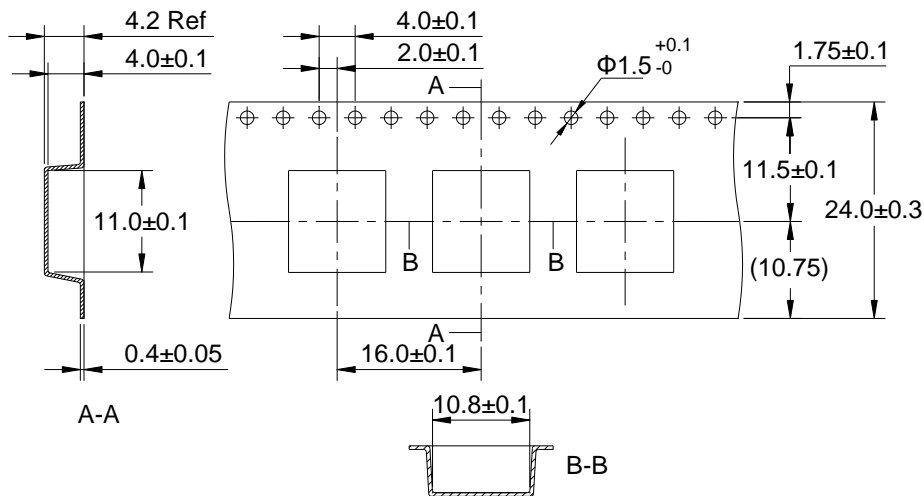
Value Code \_\_\_\_\_

## 4 Saturation Current vs Temperature Rise Current Curve



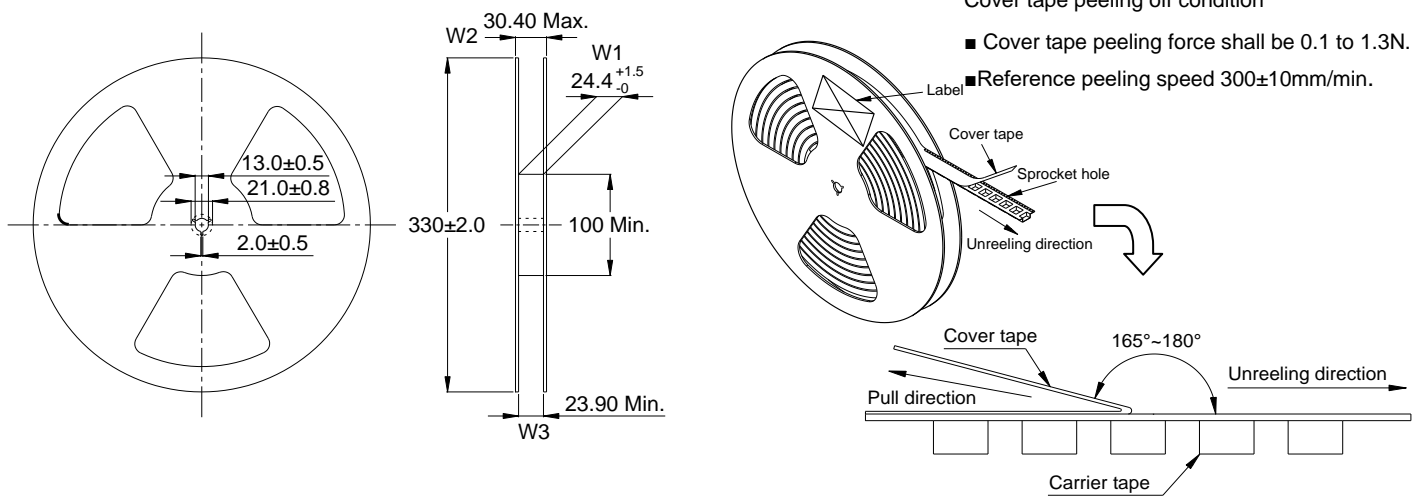
## 5 Packing Specification

### 5.1 Carrier Tape Dimensions (mm)



※ Packing is referred to the international standard IEC 60286-3.

### 5.2 Reel Dimensions (mm)



### 5.3 Carton Dimensions and Packing Quantity

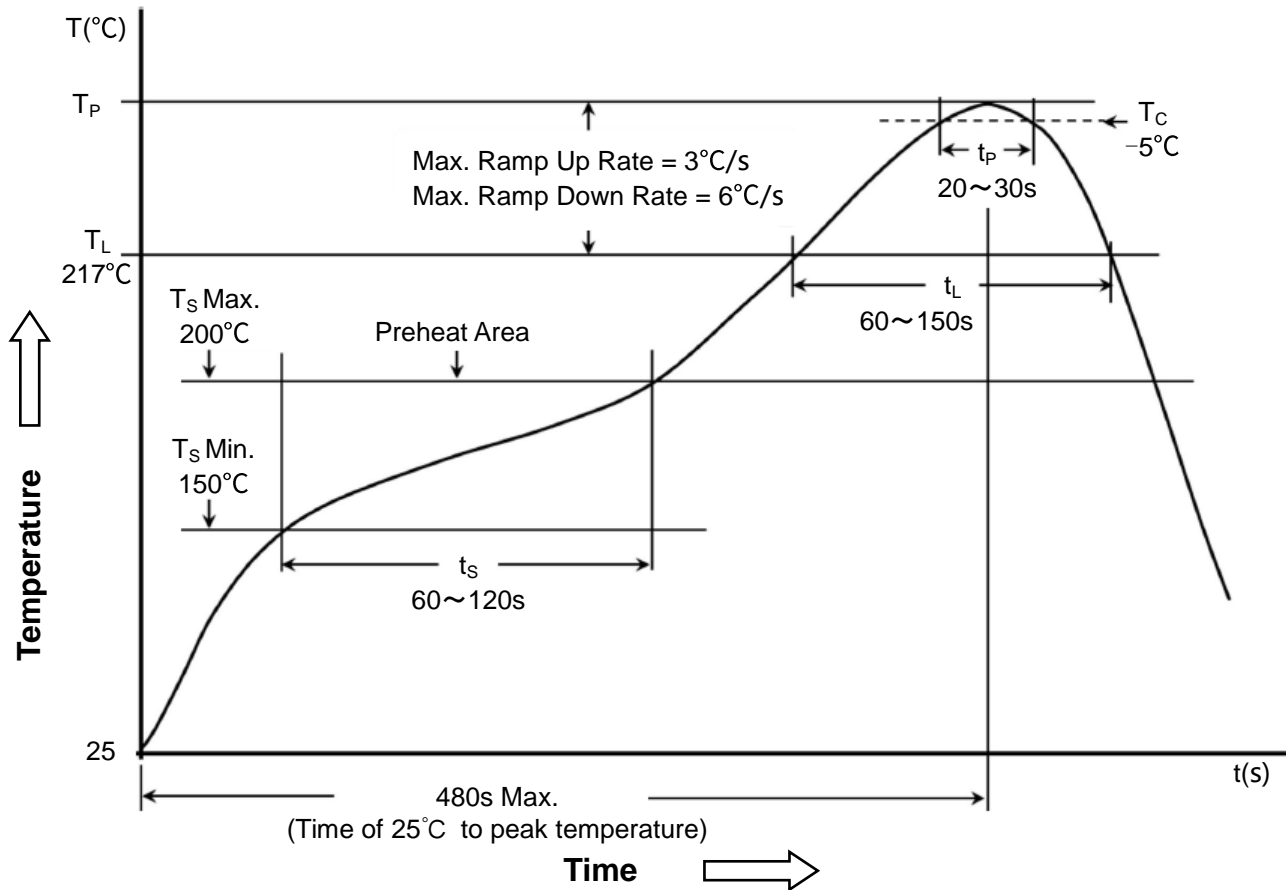
■ Inner Carton: 340×340×95mm

■ Out Carton : 355×355×385mm

Product Series	Quantity / Reel	Inner Carton Quantity	Out Carton Quantity
CSBL1030	800pcs	1600pcs = (2×800)	4800pcs = (3×1600)

## 6 Soldering Specification

### 6.1 Reflow Profile for SMT Components



### 6.2 Classification of Peak Package Body Temperature (Tp)

	Package Thickness	Package Volume		
		<350 mm <sup>3</sup>	350~2000 mm <sup>3</sup>	>2000 mm <sup>3</sup>
PB-Free Assembly	<1.6mm	260°C	260°C	260°C
	1.6~2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

※ Reflow is referred to standard IPC/JEDEC J-STD-020D.

### 7 Notice of Use

- 7.1 Special remind:Circuit design, component placement, PCB size and thickness, cooling system and etc. all will affect the product temperature. Please verify the product temperature in the final application.
- 7.2 Product in packing storage condition:temperature 5~40°C, RH≤70%.  
If taking out for use, the remaining products should be sealed in plastic bags and preserved in accordance with the above conditions, to avoid oxidation of terminals (electrodes), affecting soldering status.
- 7.3 A storage of Codaca Electronic products for longer than 12 months is not recommended, Within other effects, the terminals may suffer degradation, resulting in bad solderability. Therefore, all products shall be used within the period of 12 months based on the day of shipment.
- 7.4 Do not keep products in unsuitable storage conditions, such as areas susceptible to high temperatures, high humidity, dust or corrosion.
- 7.5 Always handle products with care.
- 7.6 Don't touch electrodes directly with bare hands as oil secretions may inhibit soldering.  
Always ensure optimum conditions for soldering.
- 7.7 When this product will be used on a similar or new project to the original one,  
sometimes it might be unable to satisfy the specifications due to different condition of usage.
- 7.8 This inductor itself does not have any protective function in abnormal condition, such as overload, short-circuit, open-circuit conditions, etc. Therefore, it shall be confirmed that there is no risk of smoke, fire, dielectric withstand voltage, insulation resistance, etc., or use in abnormal conditions protective devices or protection circuit in the end product.
- 7.9 Hi-Pot test with higher voltage than spec value will damage insulating material and shorten its life.
- 7.10 If using in potting compound, the magnet wire coating might be damaged, please consult with us.
- 7.11 Refrain from rinsing coils. If necessary, please consult with us.
- \* 7.12 Codaca Electronic products without "V" prefix are qualified for industrial product requirement , and with "V" prefix are qualified for AEC-Q200, but it doesn't mean that Codaca Electronic products can absolutely meet specific industry norms and quality test standards in automotive electronics or more strict application fields . Codaca Electronic will be exempted from being responsible for the consequences of using Codaca products in automotive electronic or higher application field related to safety when without being aware of it.